

FIG. 1 is a block diagram of a system for processing video data.

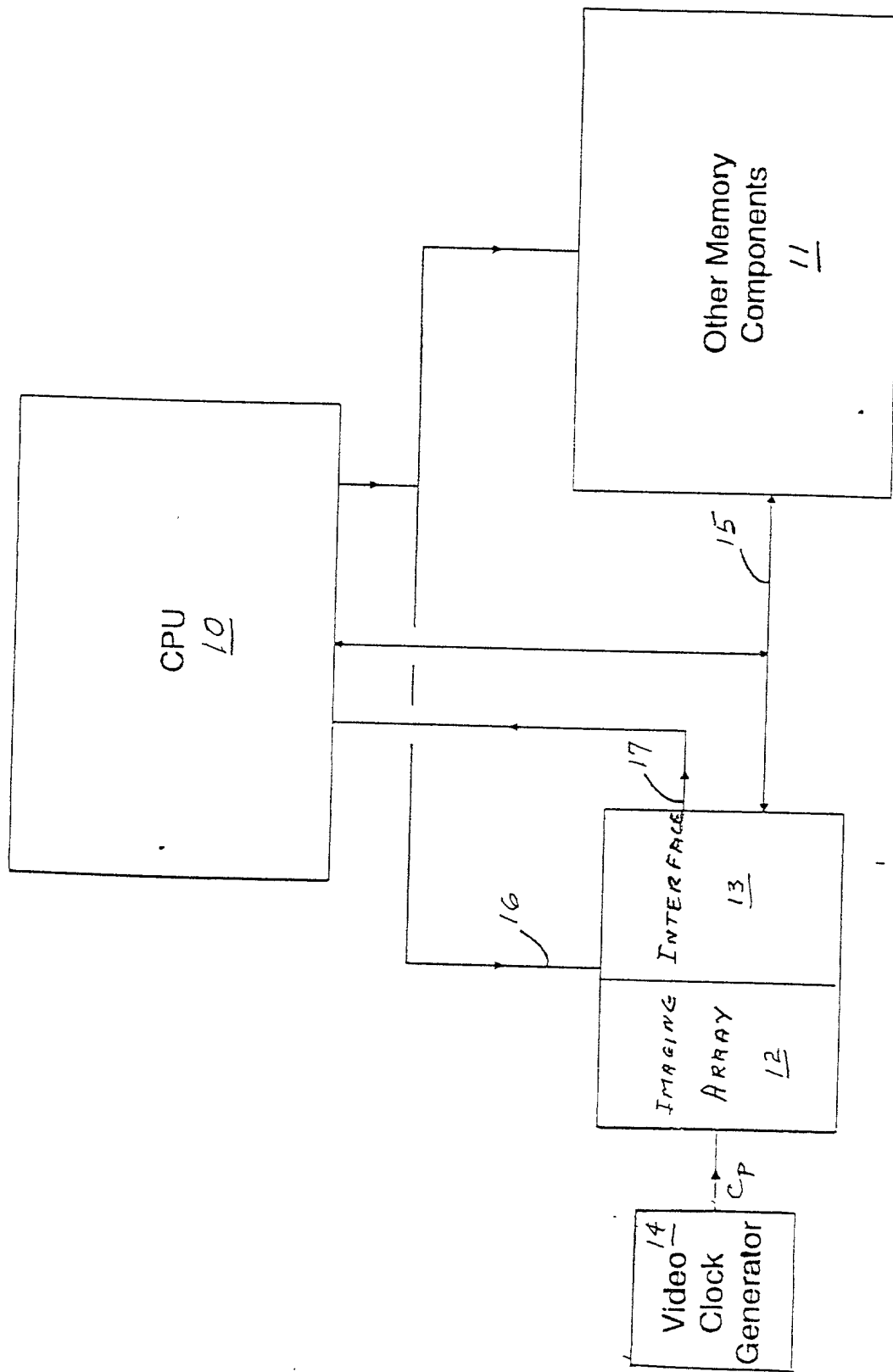


Figure 1

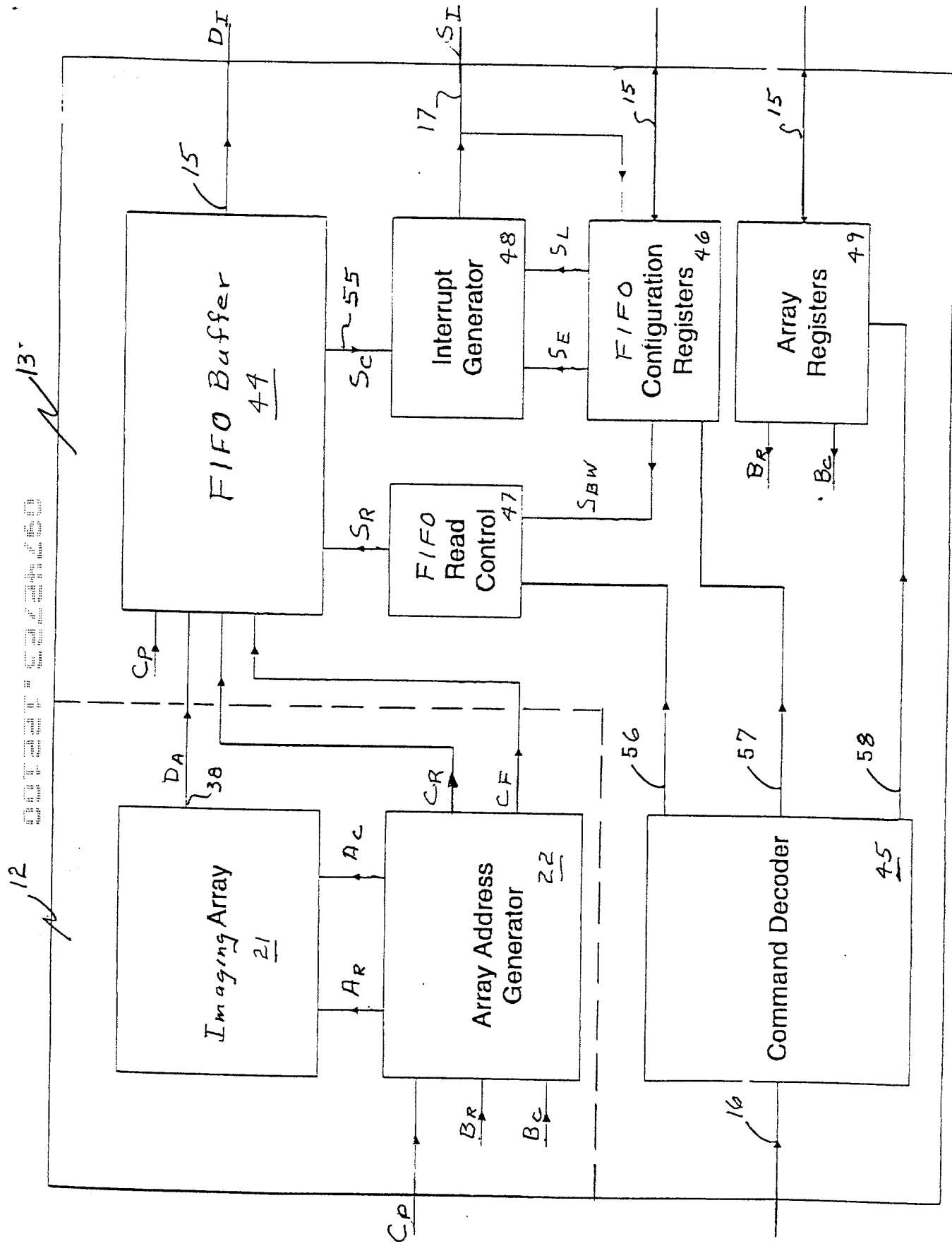


Figure 2

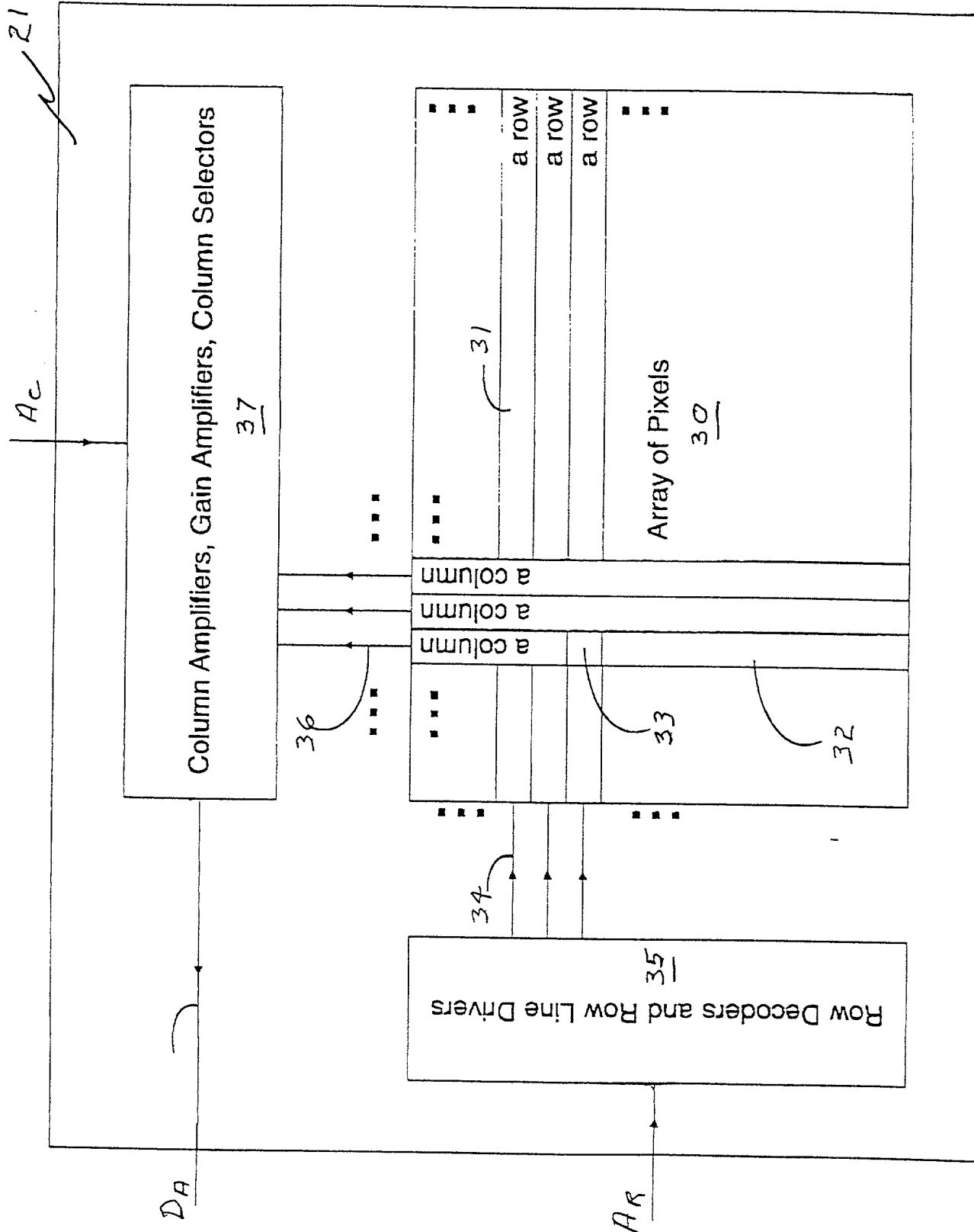


Figure 3

22

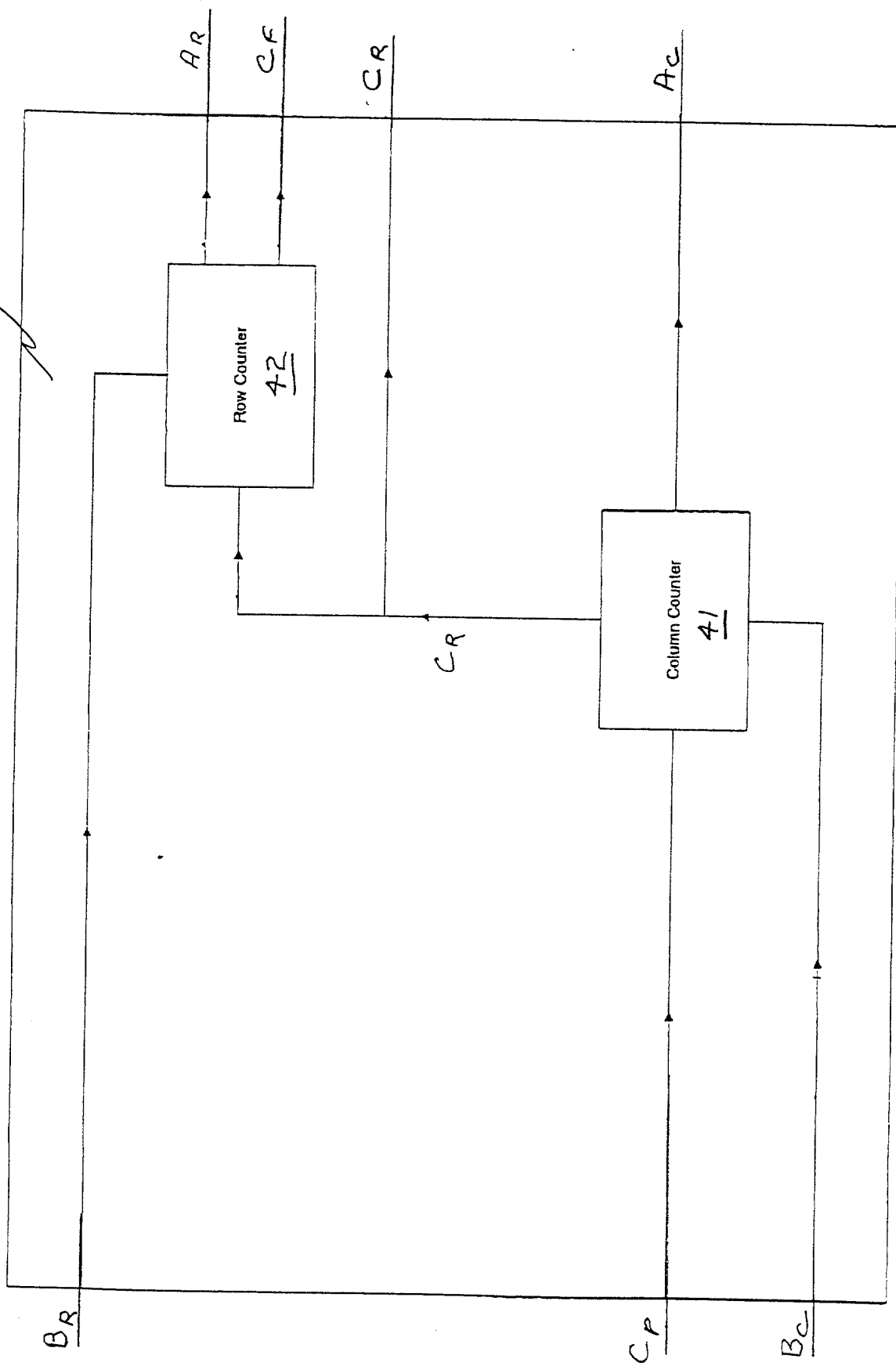


Figure 4

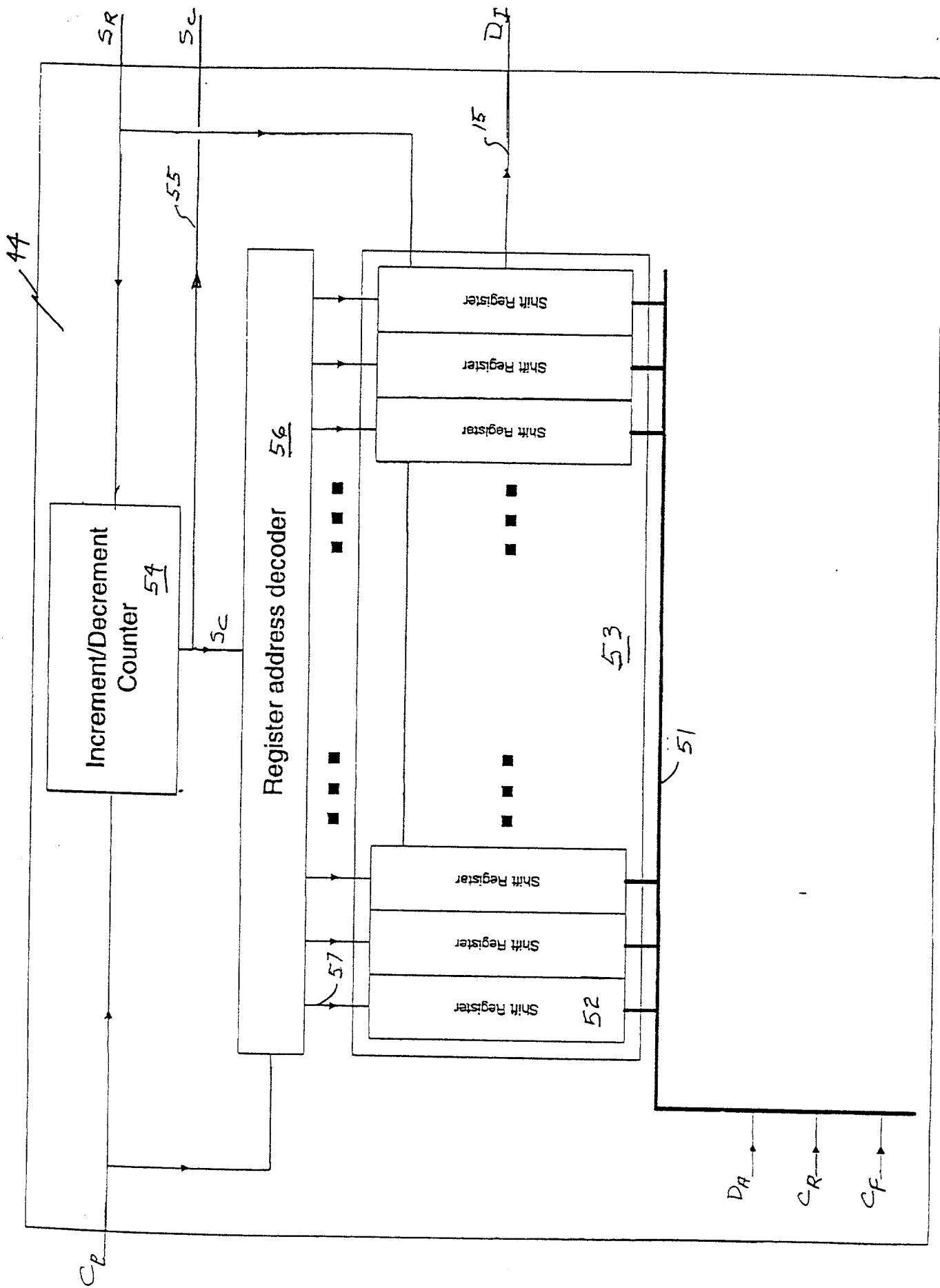


Figure 5

FIG. 6 is a block diagram of a system architecture showing the interconnection of various components.

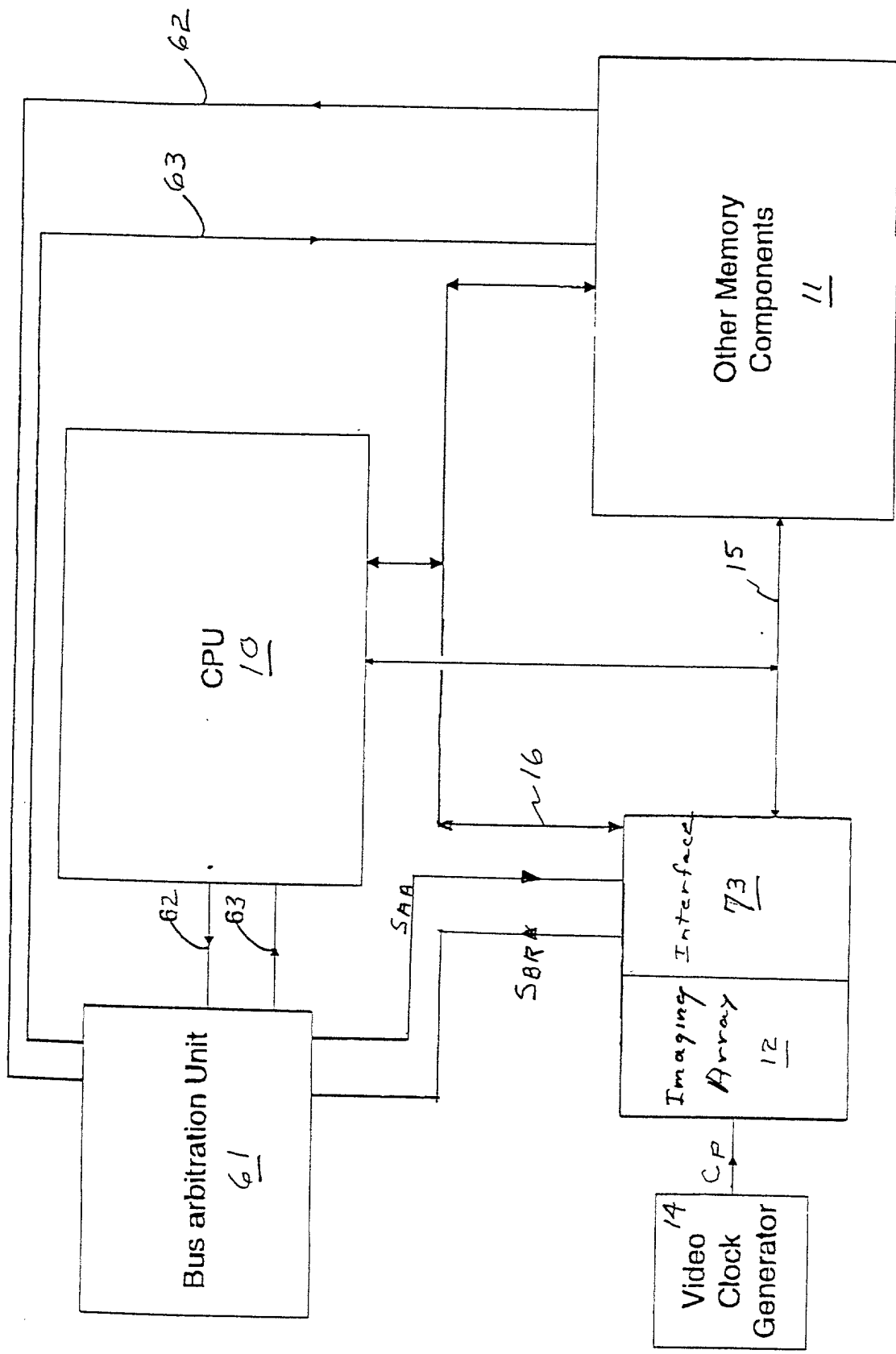


Figure 6

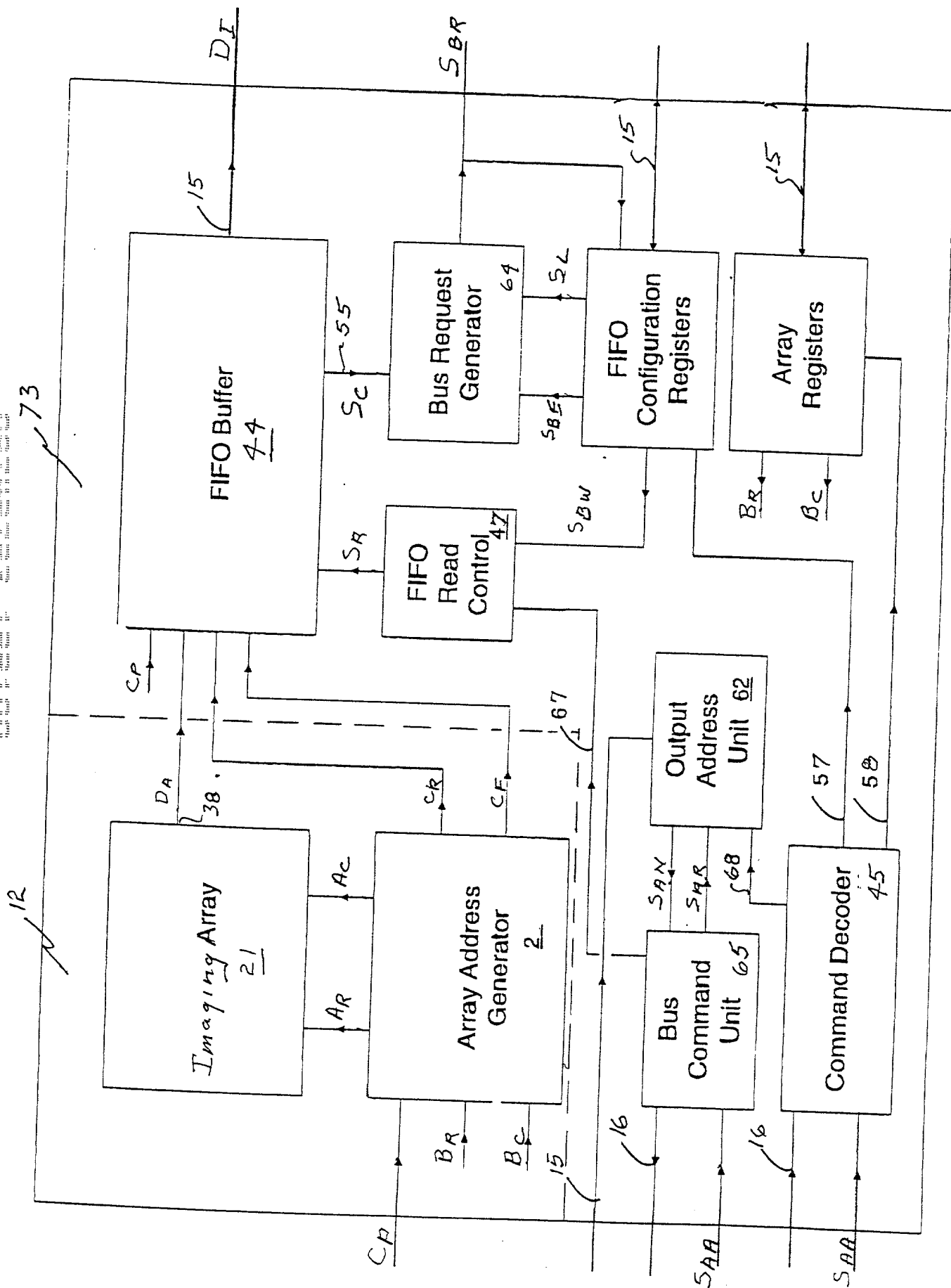


Figure 7

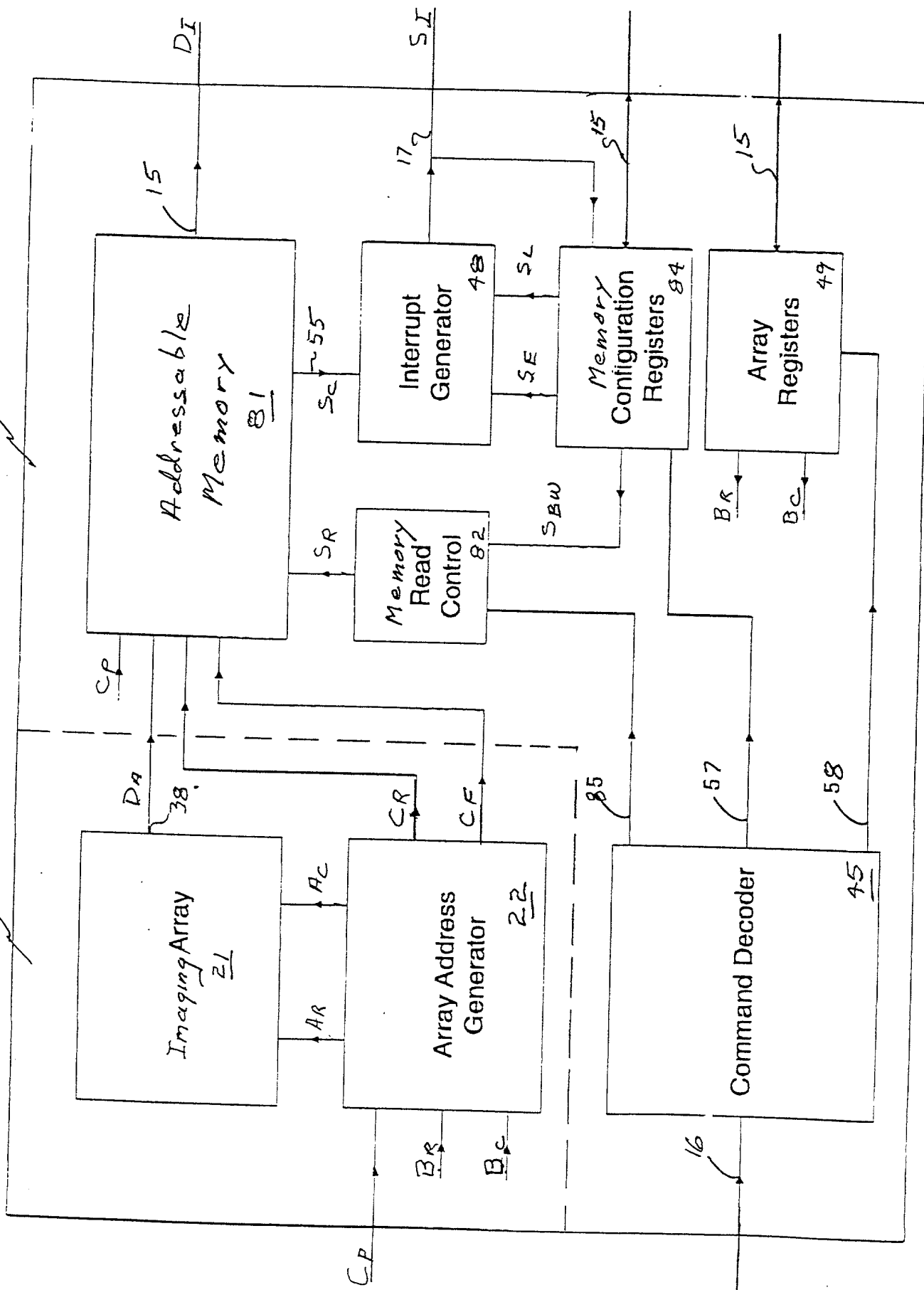


Figure 8